IN THE UNITED STATES PATENT AND TRADEMARK OFFICE Application Branch

In re Patent Application of		
Applicants: Kazuo SUTO)	
Serial No.: Not Yet Known	,	IINARY DMENT
Filed : CONCURRENTLY)	TANDER SENIO DESIGNATION DE CONTRATOR
For : MODULATOR)	26694
Attorney Docket: 32014-177530	ý	PATENT TRADEMARK OFFICE
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	January 24, 2002	
Assistant Commissioner for Patents		
Washington, D.C. 20231	/	
Sir:		
70	101 1 4 6 0 111 6	11

Prior to examination please amend Claims 4, 6, 9 and 11 as follows:

4. (amended) The modulator as claimed in Claim 2, wherein the PLL circuit includes an oscillator that generates the reference signal, a frequency divider that divides the frequency of the input signal to output a frequency divided signal, and a comparator that compares the reference signal and the frequency divided signal to detect a phase difference between both.

6. (amended) The modulator as claimed in Claim 2, wherein the AGC circuit outputs the control signal on the basis of the signal outputted from the PLL circuit.

9. (amended) The modulator as claimed in Claim 7, wherein the PLL circuit includes an oscillator that generates the reference signal, a frequency divider that divides the frequency of the input signal to output a frequency divided signal, and a comparator that compares the reference signal and the frequency divided signal to detect a phase difference between both.